

FIG. 1

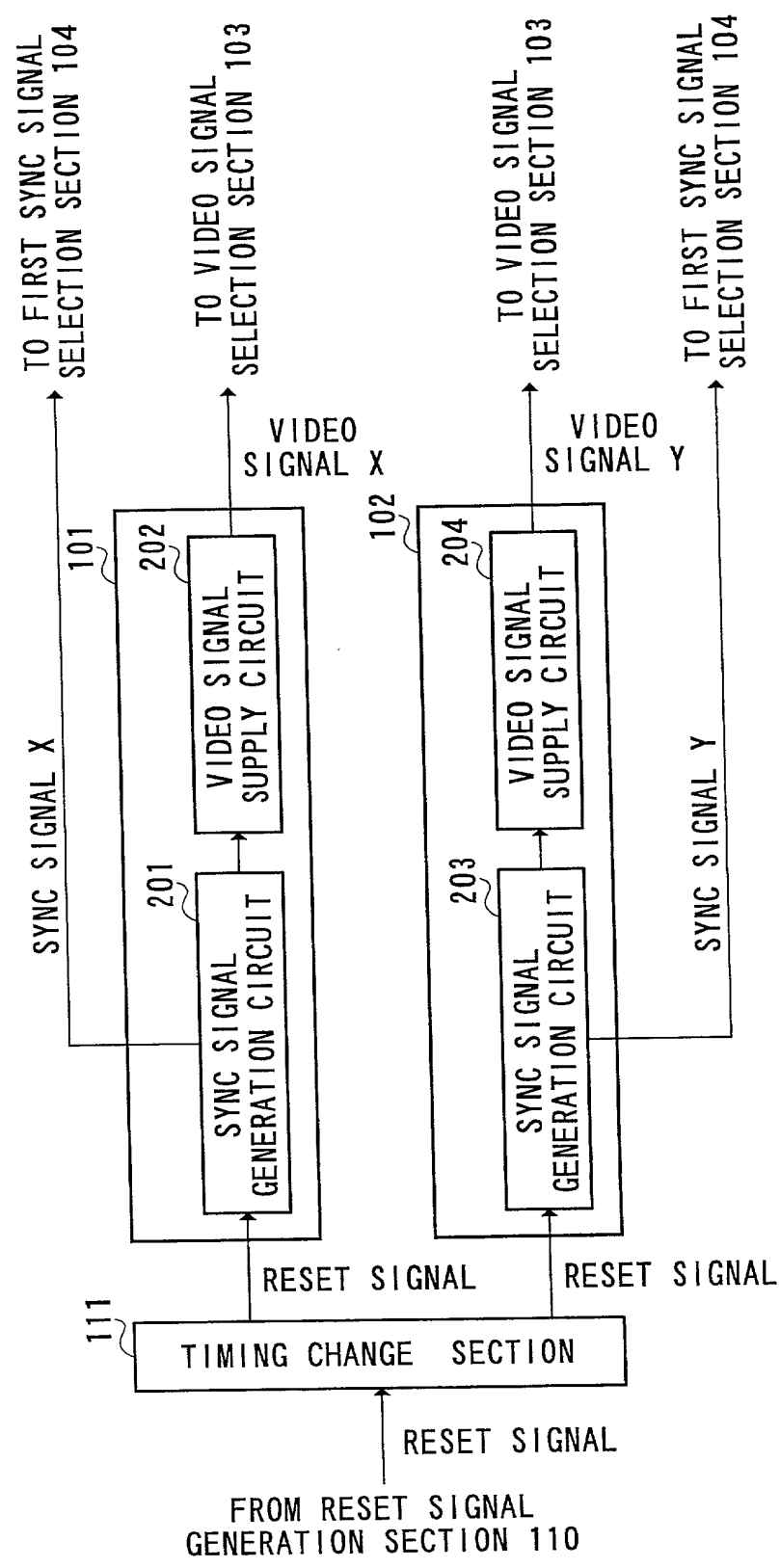


FIG. 2

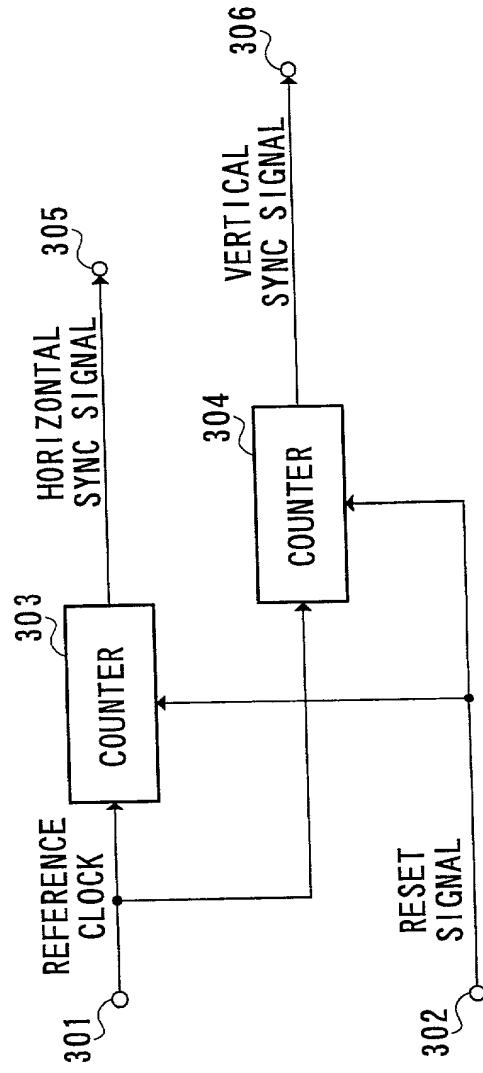


FIG. 3

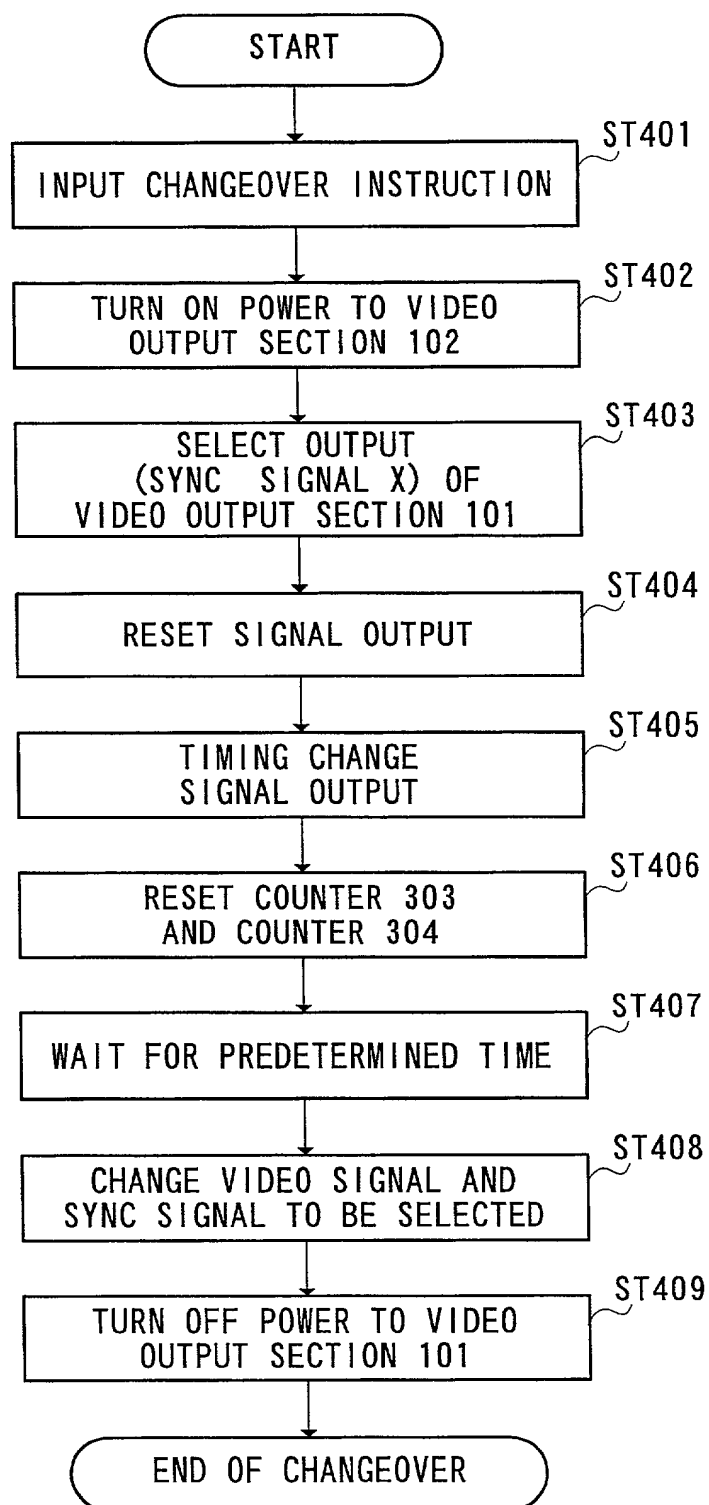


FIG. 4

FIG. 5A REFERENCE CLOCK

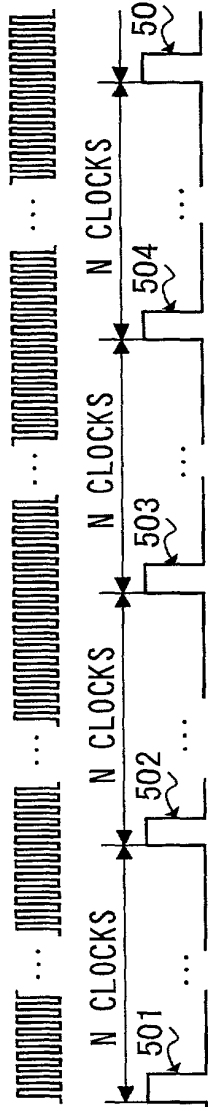


FIG. 5B VERTICAL SYNC SIGNAL OUTPUT BY VIDEO SECTION 101 (VSYNC)

FIG. 5C RESET SIGNAL GENERATION INSTRUCTION OUTPUT BY VIDEO OUTPUT CONTROL SECTION 108

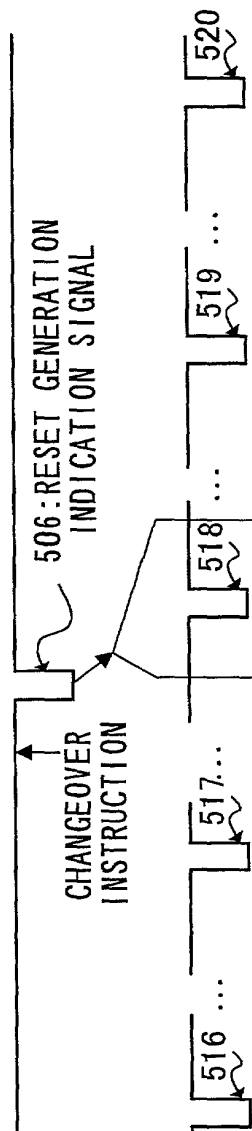


FIG. 5D SIGNAL INVERTING VERTICAL SYNC SIGNAL INPUT TO RESET SIGNAL GENERATION SECTION 110 (VSYNC')

FIG. 5E RESET SIGNAL

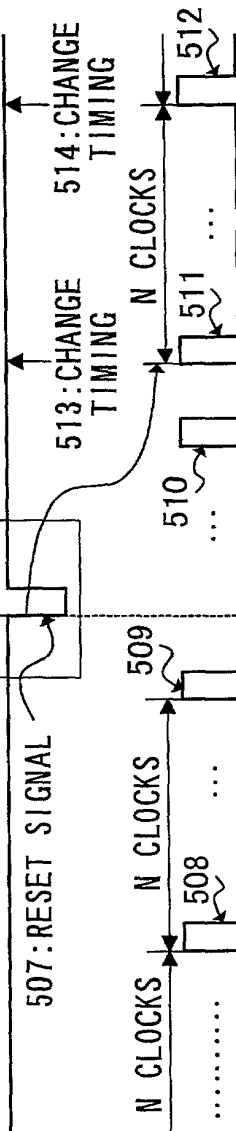
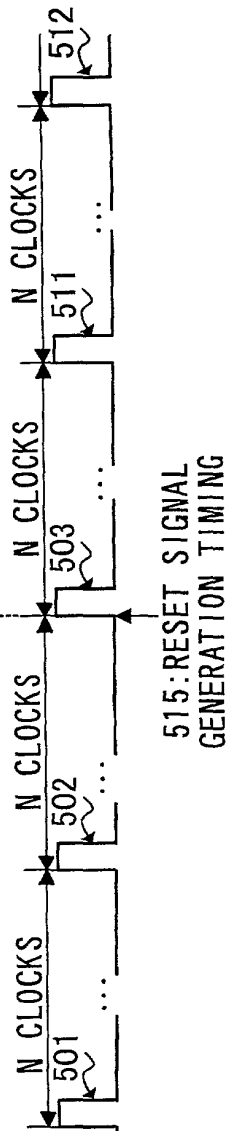


FIG. 5F VERTICAL SYNC SIGNAL OUTPUT BY VIDEO SECTION 102

FIG. 5G VERTICAL SYNC SIGNAL OUTPUT TO OUTPUT TERMINAL 105



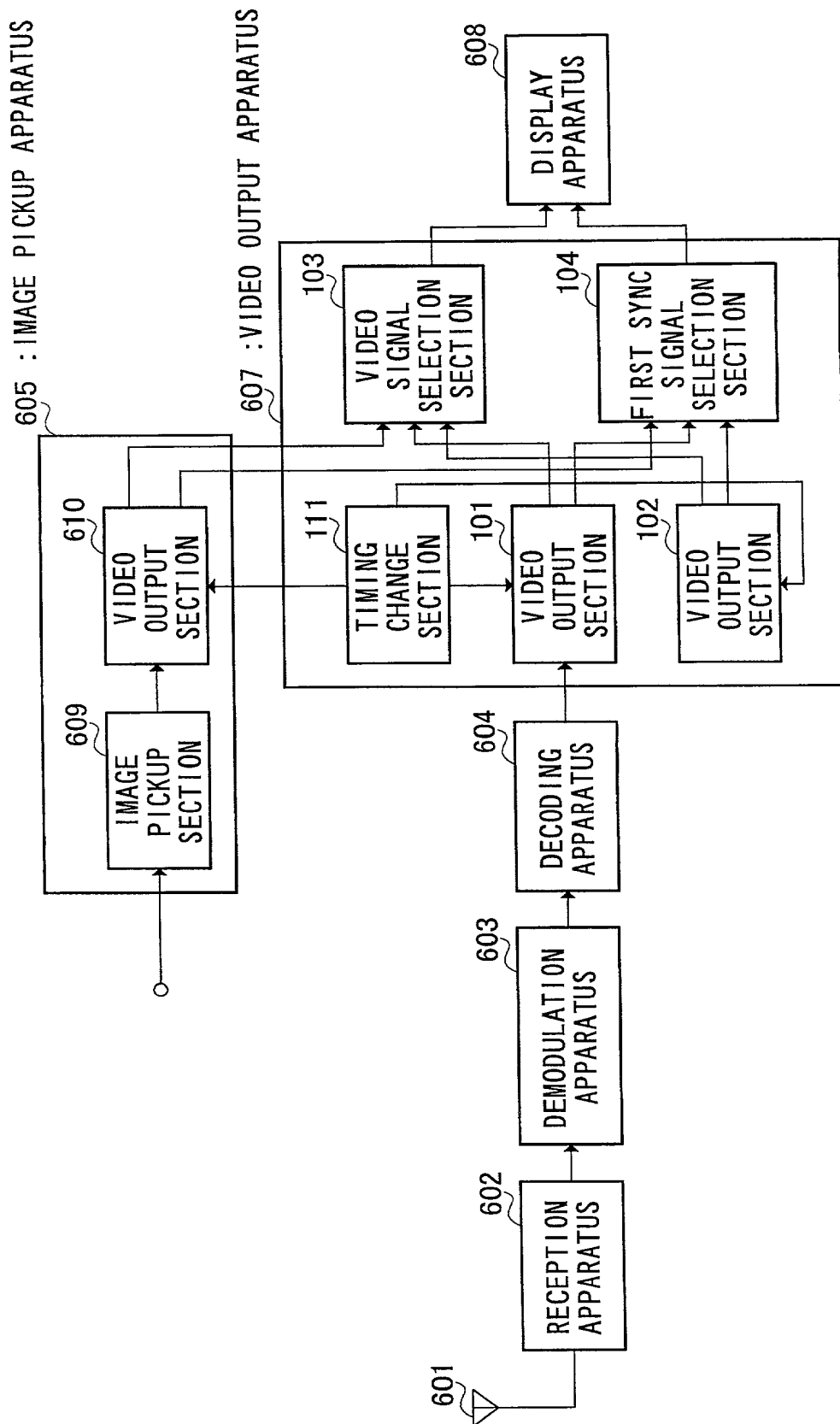


FIG. 6